

Notice of References Cited	Application/Control No. 10/523,216		Applicant(s)/Patent Under Reexamination CLEMENS ET AL.	
	Examiner Matthew W. Such		Art Unit 2891	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,133,066	10-2000	Murakami, Tomoo	438/108
*	B	US-6,400,024	06-2002	Drury et al.	257/759
*	C	US-5,457,881	10-1995	Schmidt, Walter	29/852
*	D	US-7,244,669	07-2007	Sirringhaus et al.	438/535
*	E	US-7,176,040	02-2007	Sirringhaus et al.	438/6
*	F	US-5,985,752	11-1999	Chang, Ming-Lun	438/637
*	G	US-2002/0129894	09-2002	Liu et al.	156/291
*	H	US-2002/0004180	01-2002	Hotta et al.	430/311
*	I	US-2003/0186558	10-2003	Brenner, Michael F.	438/760
*	J	US-4,914,056	04-1990	Okumura, Katsuya	438/629
*	K	US-2003/0029831	02-2003	Kawase, Takeo	216/27
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kawase, T., et al. "Inkjet Printed Via-Hole Interconnections and Resistors for All-Polymer Transistor Circuits." ADV. MATER., Vol. 13, No. 21 (2 November 2001): pp. 1601-1605.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.